

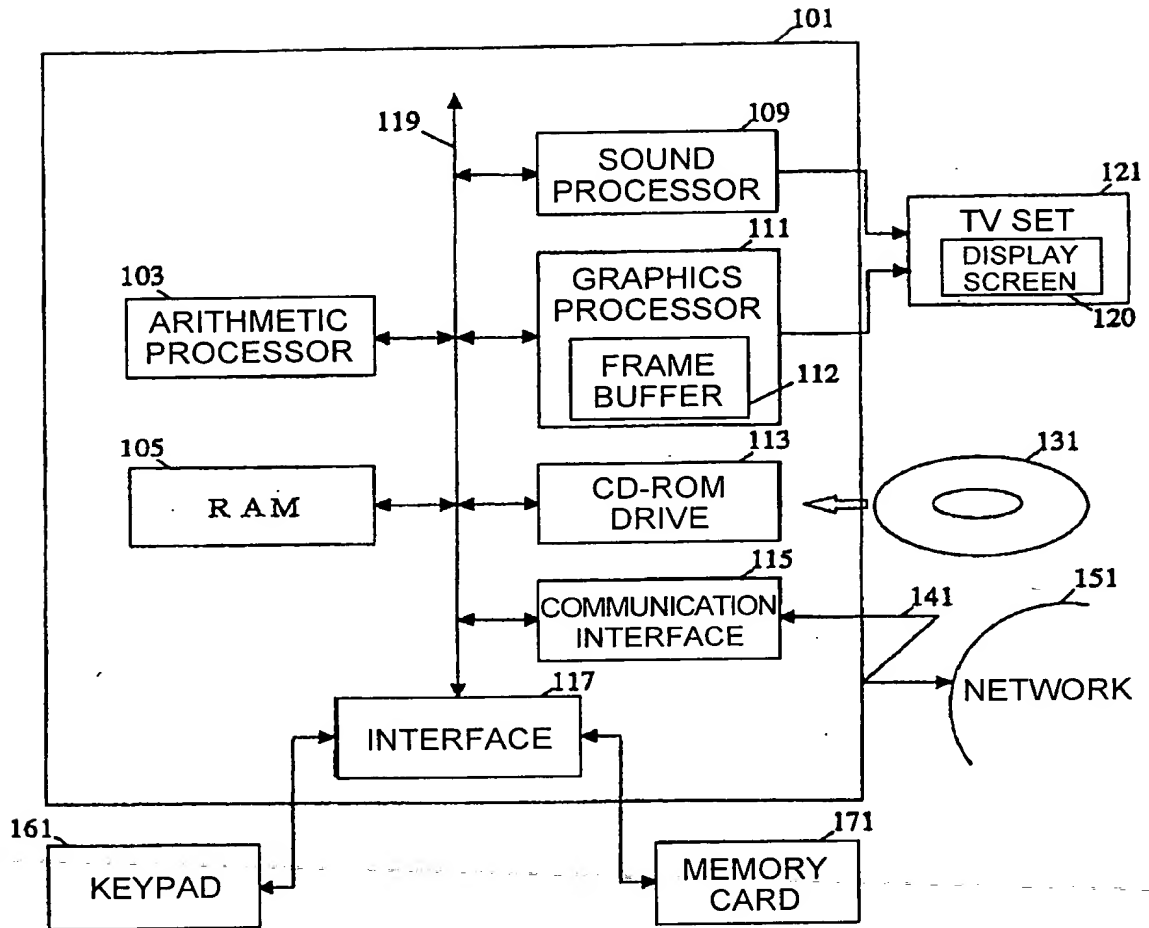
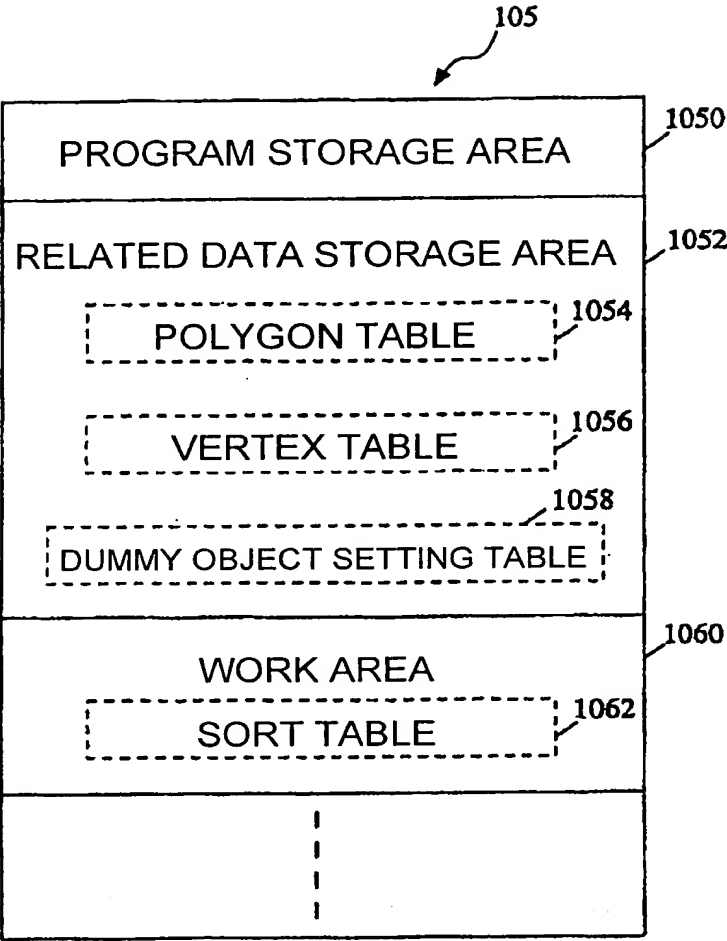
Fig.1

Fig.2



00222T E6ET5260

Fig.3

1054		
541	543	545
OBJECT ID No.	POLYGON ID No.	VERTEX ID No.
M1	P1	V1
		V2
		V3
	P2	V3
		V2
		V4
	P3	V4
		V5
		V3
	⋮	⋮
⋮	⋮	⋮

002221 E6ET5260

0975163 1220

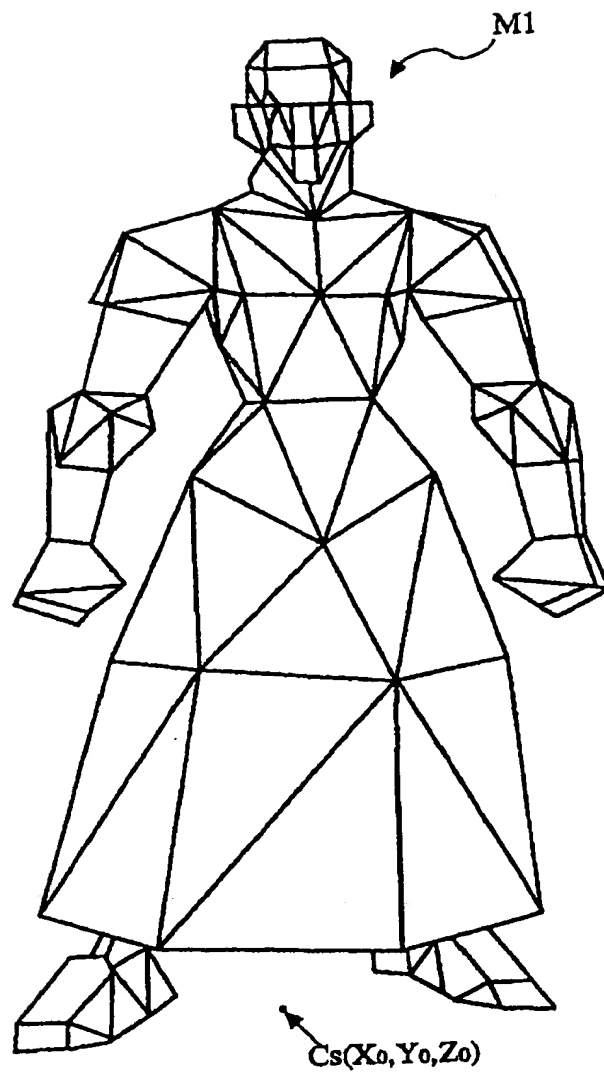


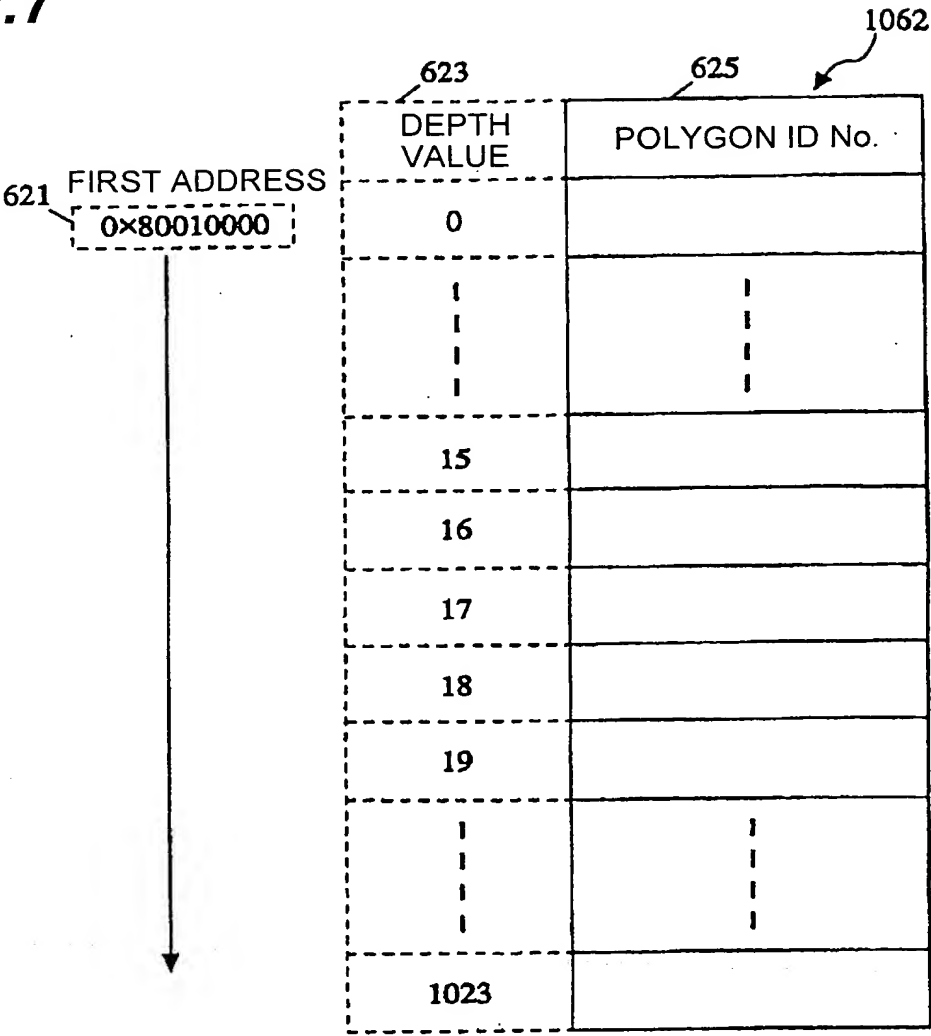
Fig.5

1056

561 OBJECT ID No.	563 VERTEX ID No.	565 COORDINATE DATA	567 TEXTURE DATA
M1	V1	(X1,Y1,Z1)	(U1,V1)
	V2	(X2,Y2,Z2)	(U2,V2)
	V3	(X3,Y3,Z3)	(U3,V3)
	V4	(X4,Y4,Z4)	(U4,V4)
	V5	(X5,Y5,Z5)	(U5,V5)
	⋮	⋮	⋮
⋮	⋮	⋮	⋮

581	585	1058	587	589
OBJECT ID No.	LIGHTNESS ADJUSTMENT VALUES	COORDINATE ADJUSTMENT VALUES	DEPTH ADJUSTMENT VALUES	
M1	Rd,Gd,Bd	(Xd,Yd,Zd)	Dd	
M3	Rc,Gc,Bc	(Xc,Yc,Zc)	Dc	
M8	Rf,Gf,Bf	(Xf,Yf,Zf)	Df	
⋮	⋮	⋮	⋮	

Fig.7



00/222T"E6ET5/60

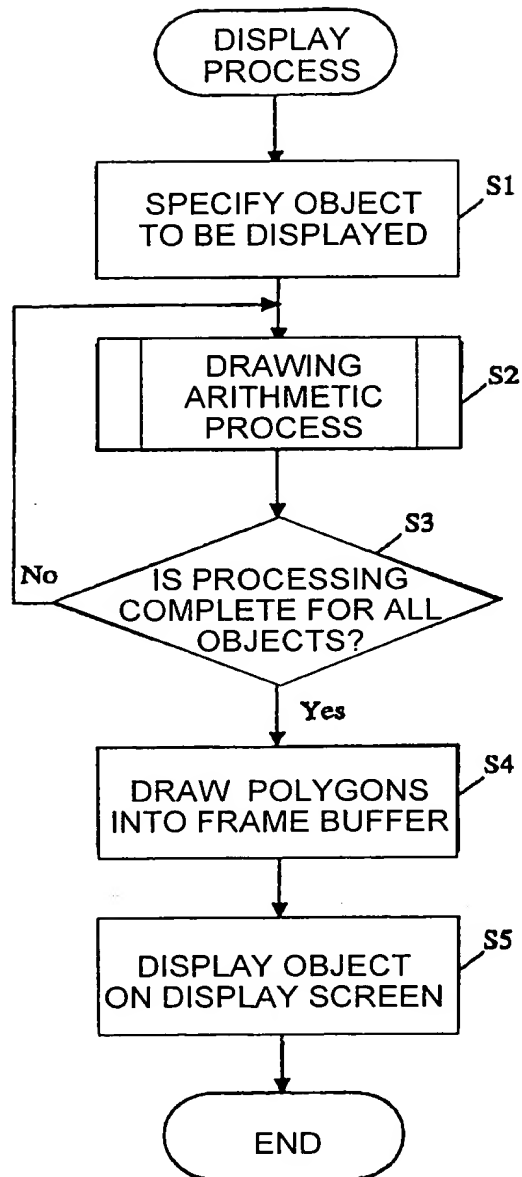
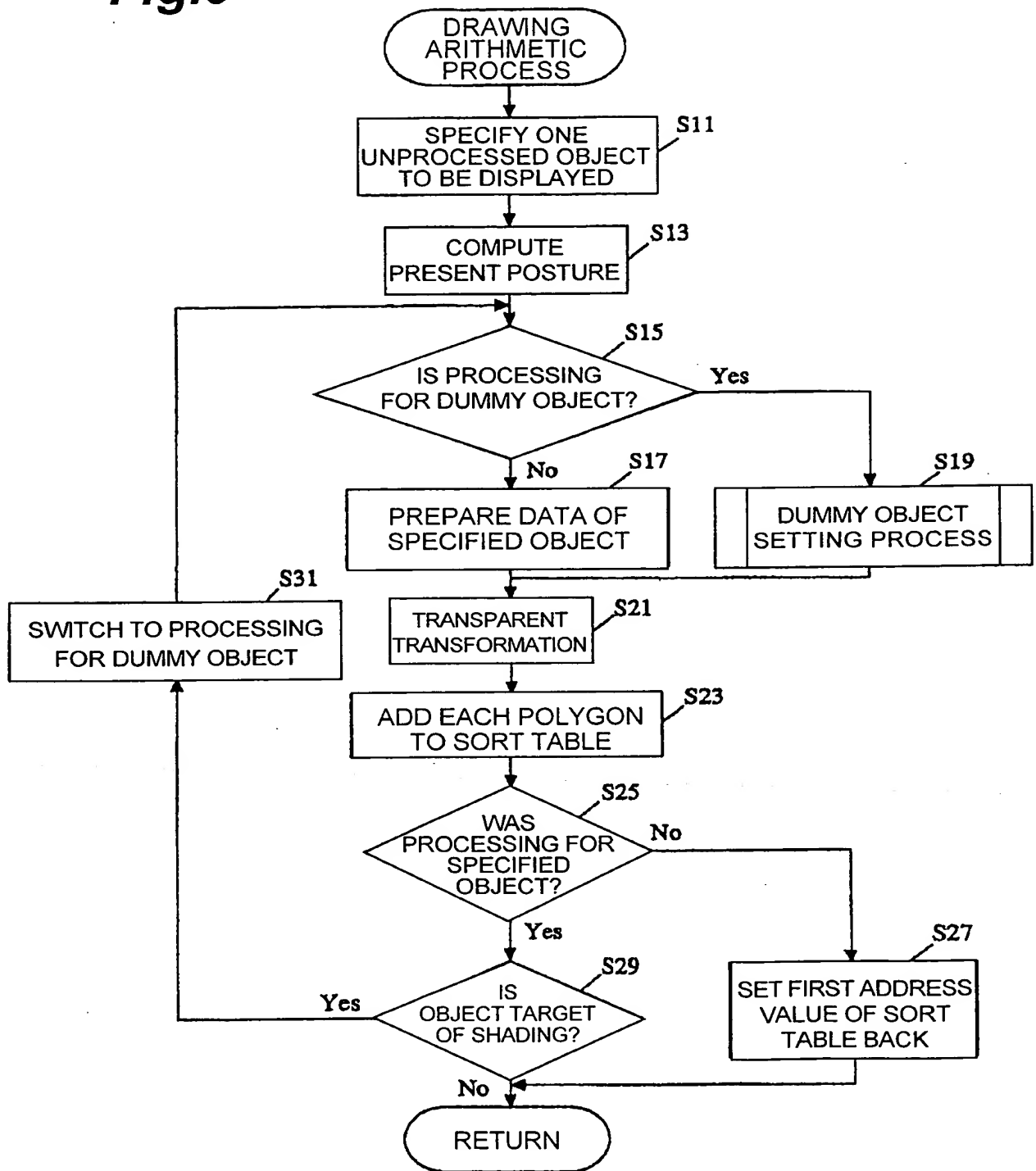
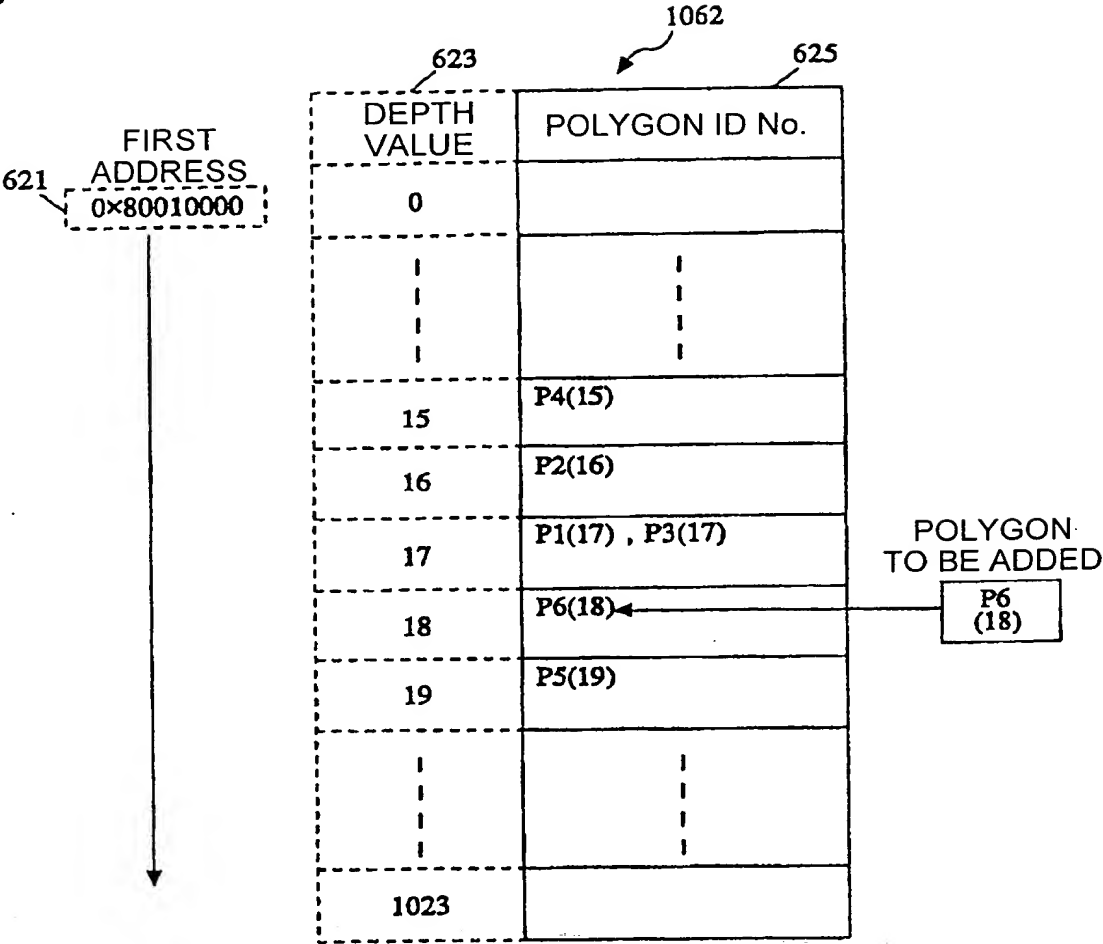
Fig.8

Fig.9



00751393 122700

Fig.10



00/22T E6ET5/60

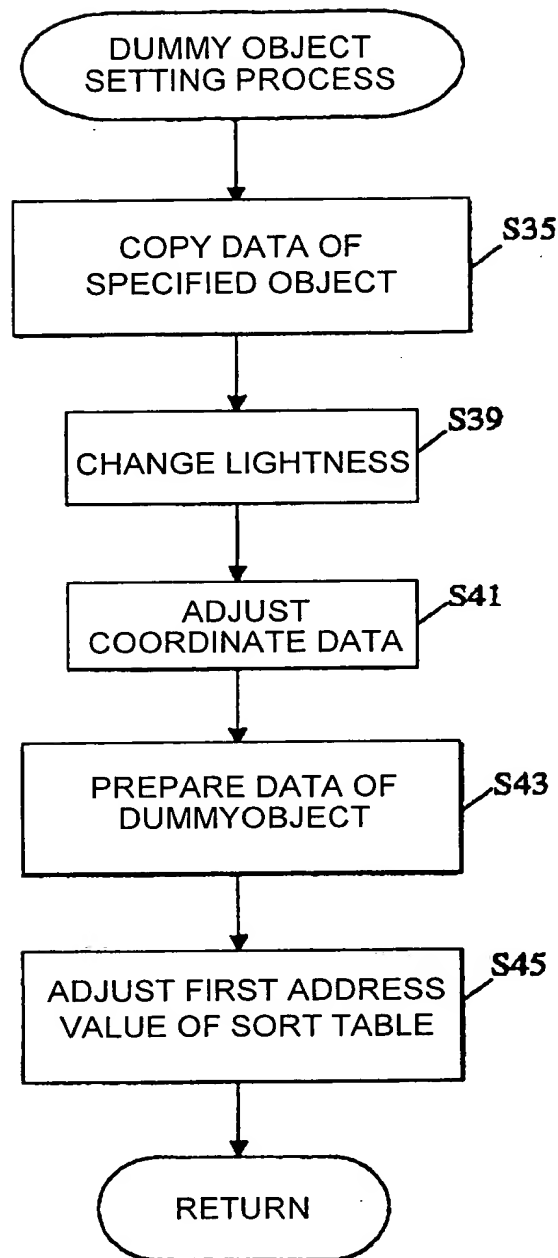
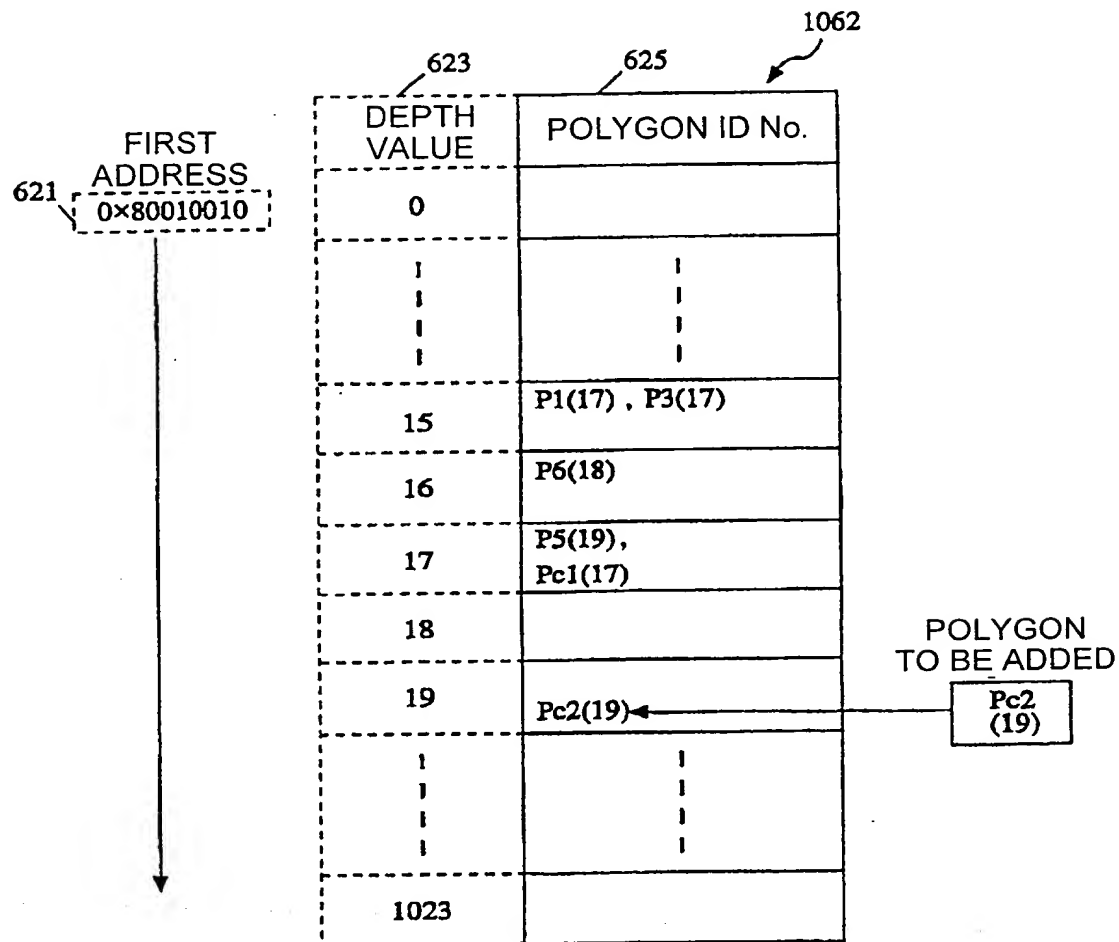
Fig.11

Fig.12



004221" E6EFS/60

Fig.13

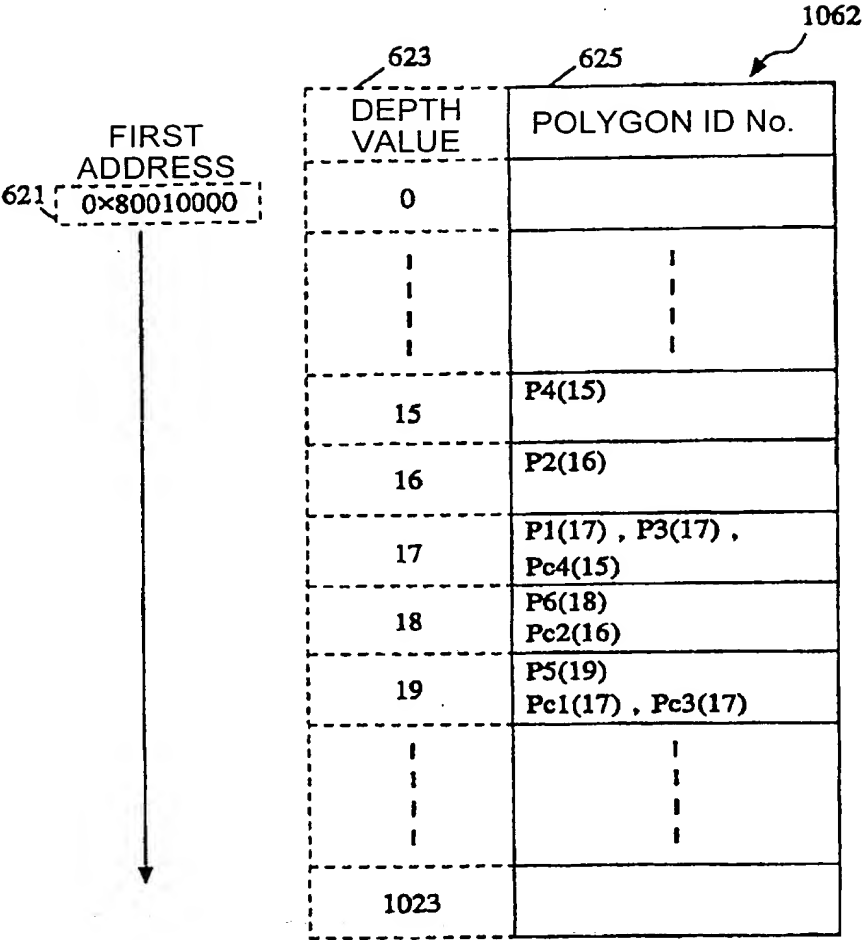
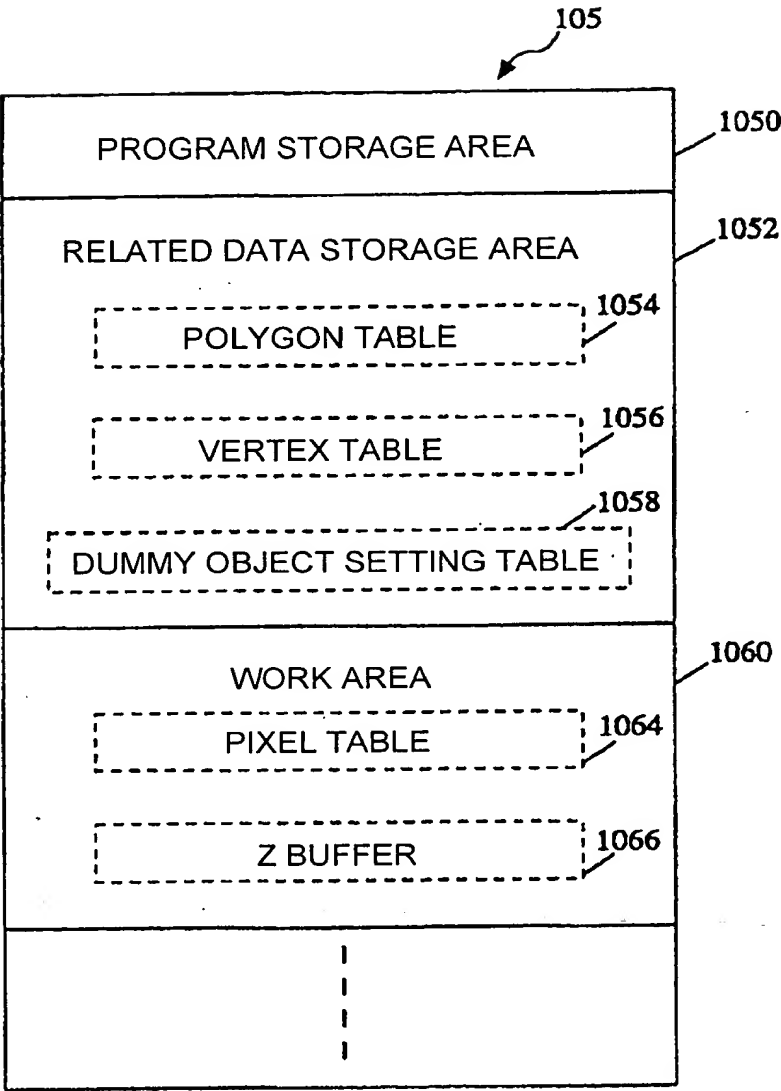


Fig.14



09751393.122700

Fig.15

641	643	1064
PIXEL ID No.	COLOR DATA (R,G,B)	
1		
2		
3		
⋮	⋮	
⋮	⋮	
⋮	⋮	
⋮	⋮	

Fig.16

Fig. 16 is a schematic diagram of a memory array 120. The array is organized into rows and columns. The first row is labeled with indices 0, 1, 2, followed by four dashes, and then 318, 319. The second row is labeled with indices 320, 321, 322, followed by four dashes, and then 638, 639. The array is divided into a large central block and a bottom row. A bracket on the left side of the array is labeled 240. A bracket at the bottom of the array is labeled 320. An arrow points to the top right corner of the array, labeled 120.

Fig.17

1066	
661	663
PIXEL ID No.	Z VALUE
1	
2	
3	
⋮	⋮
⋮	⋮
⋮	⋮
⋮	⋮

09751393-122700

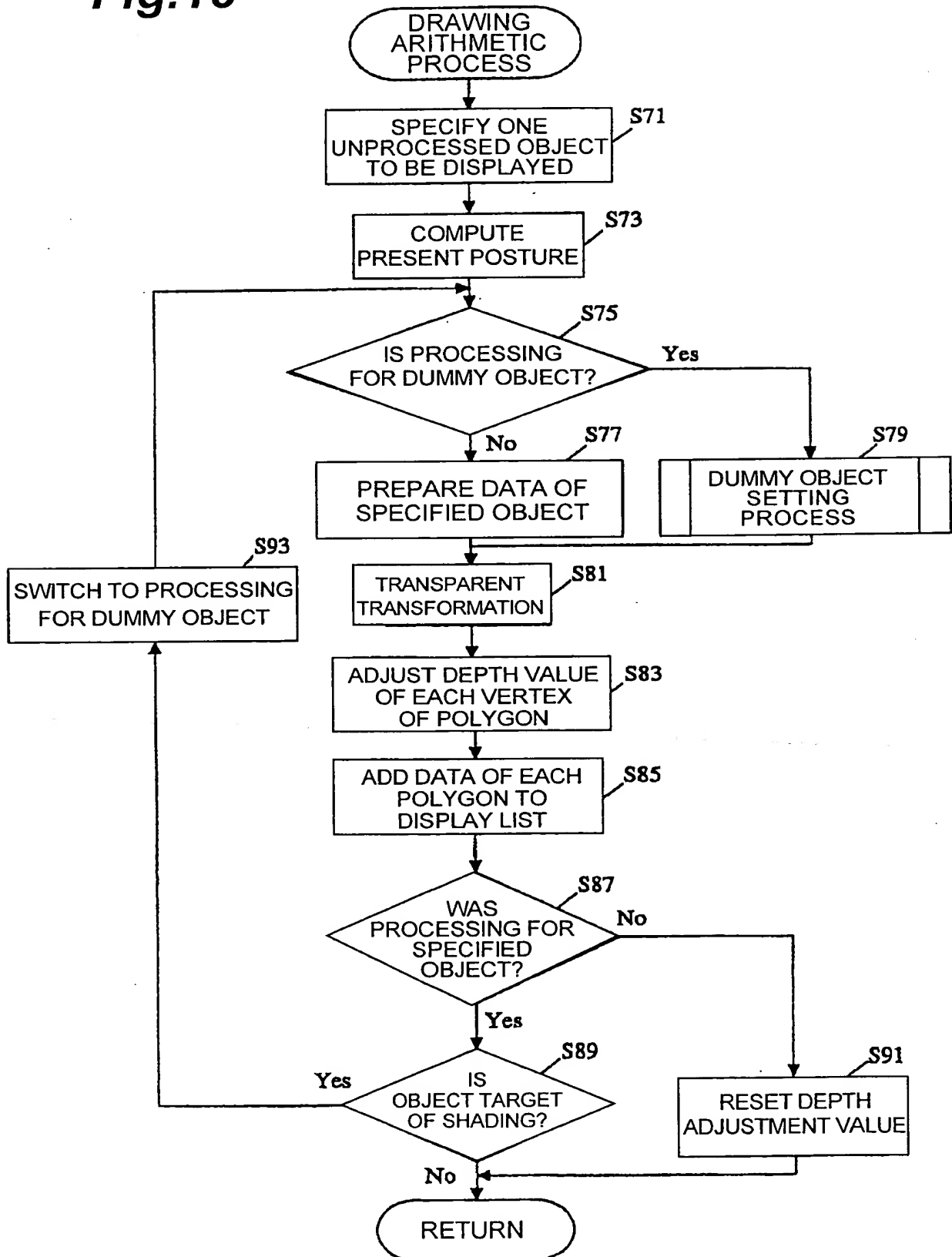
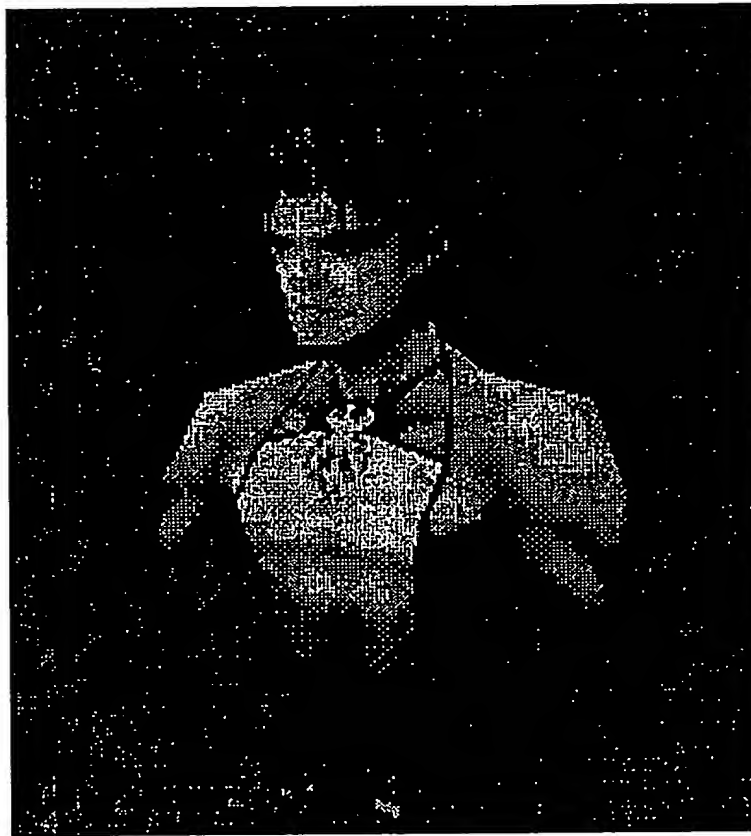


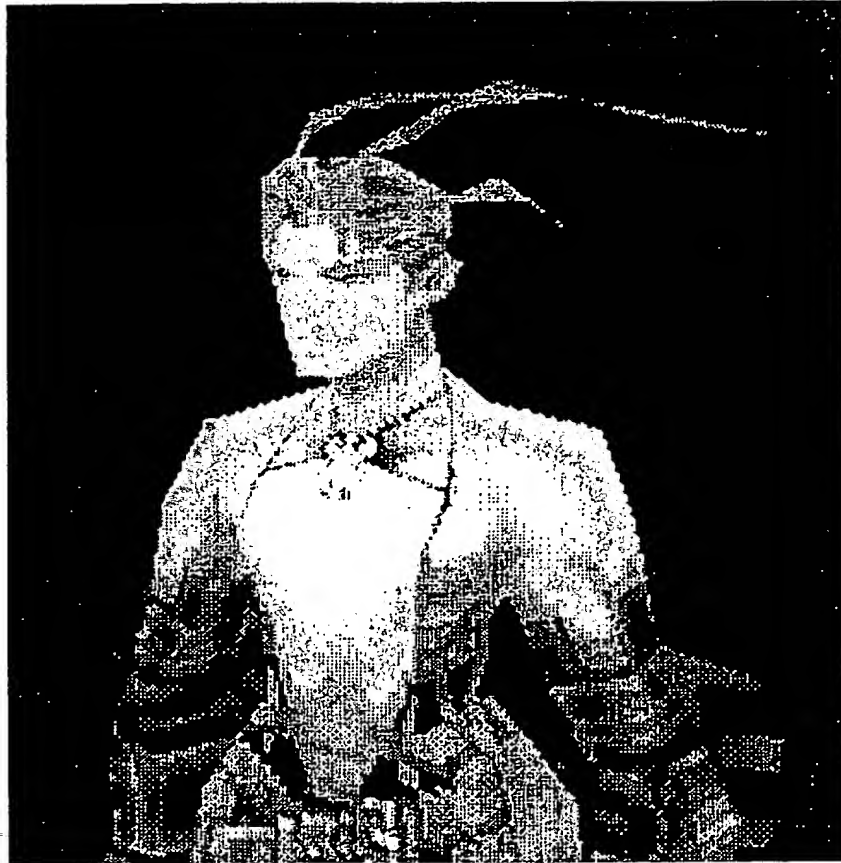
Fig.20



120

09751393 122700

Fig.21



120

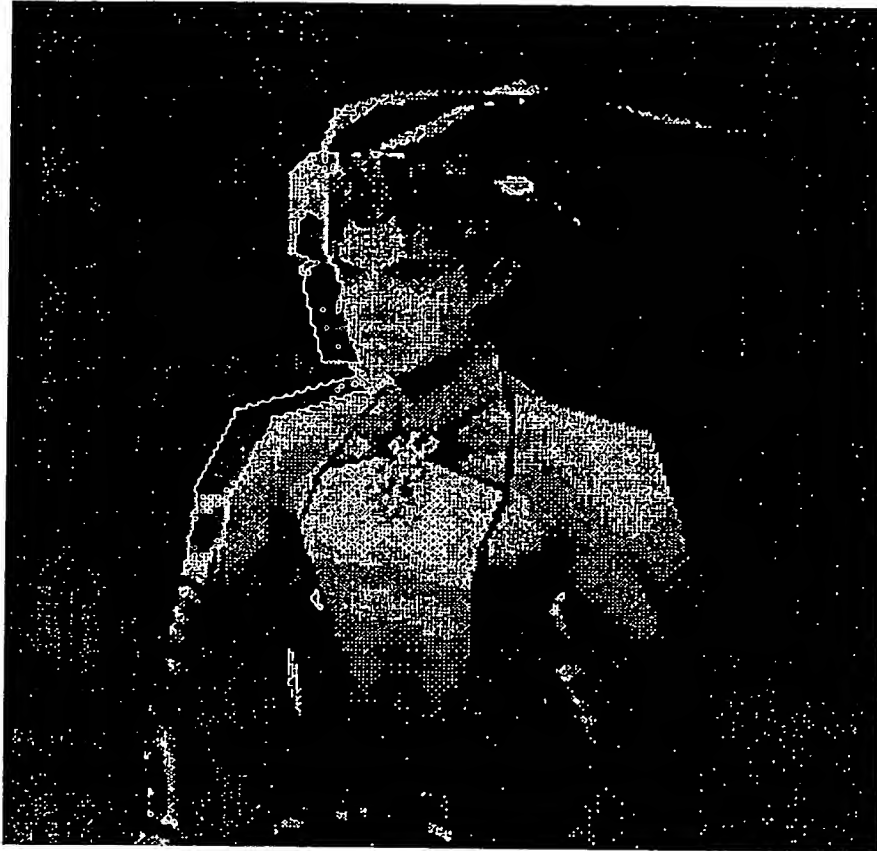
00422T" E6ETS260

Fig.22



00/22T" E6ET5/60

Fig.23



120

00222T E66T560